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		Application Number		10/066,095		
TRANSMIT	Filing Date		1/31/2002			
FORM		First Named Inventor		Steven Teig, et al.		
(to be used for all correspondence after initial filing)		Group Art Unit		2825		
		Examiner Name		Brandon Bowers		
Total Number of Pages in This Sul	omission	Attorney Docket Number		SPLX.P0074		
	Enclosure	s (Check all that appl	y)			
Fee Transmittal Form	Assignme (for an ap	ent Papers plication)	☐ After Grou	Allowance Communication to p		
Fee Attached	Drawing(s	s)		al Communication to Board of als and Interferences		
Amendment/Reply	Licensing	Licensing-related Papers		al Communication to Group al Notice, Brief, Reply Brief)		
After Final	Petition		Prop	rietary Information		
Affidavits/declarations	61 11 1	Petition to Convert to a Provisional Application		us Letter		
Preliminary Amendment						
Extension of Time Request		Power of Attorney, Revocation Change of Correspondence Address		r Enclosures (<i>Listed below</i>):		
Express Abandonment Request	Terminal	Terminal Disclaimer		ferences		
Request to Rescind Previous Non-Publication Request		or Refund				
Information Disclosure Statement and PTO-1449 CD, Num		ber of CD(s)				
Certified Copy of Priority Documents	Remarks:					
Response to Missing Parts, Incomplete Application						
Response to Missing Parts under 37 CFR 1.52 or 1.53						
S	IGNATURE OF AP	PLICANT, ATTORNEY,	OR AGENT	•		
Firm Or Individual name Mani Adeli, Regl No. 39,585 of Stattler Johansen & Adeli LLP						
Signature						
Date August 21, 2004						
CERTIFICATE OF TRANSMISSION/MAILING						
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first classimal in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date: 8/24/04						
Signature	V		Date	August 24, 2004		

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Brandon Bowers

Group Art Unit: 2825

In the application of:

Steven Teig, et al.

Serial No.: 10/066,095

Filing Date: 1/31/2002

For: METHOD AND APPARATUS FOR

IDENTIFYING A SET OF PATHS

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to

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Dated: August 24, 2004

Respectfully submitted

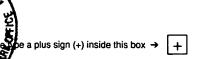
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Complete if Known Substitute for form 1449A/PTO **Application Number** 10/066,095 INFORMATION DISCLOSURE Filing Date 1/31/2002 STATEMENT BY APPLICANT First Named Inventor Steven Teig, et al. (use as many sheets as necessary) 2825 **Group Art Unit Examiner Name Brandon Bowers** SPLX.P0074 Sheet of 1 Attorney Docket Number

FOREIGN PATENT DOCUMENTS								
Examiner* Initials	Cite No.1	For Office ³	reign Patent Do Number ⁴	cument Kind Code (if known) ⁵	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	1.	JP	02-262354	I	10-25-1990	Kuribayashi, Mototaka		

· · · · · ·		NON PATENT LITERATURE DOCUMENTS	
Examiner* Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
	2.	CHO J.D., Wiring Space and Length Estimation in Two-Dimensional Arrays, May 2000, Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on, VOL. 19, Iss. 5, pp. 612-615.	
	3.	CONG J. et al., DUNE – A Multilayer Gridless Routing System, May 2001, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 20, iss. 5, pp. 633-647.	
	4.	DION J. et al., Contour: A Tile-based Gridless Router, March 1995, Digital Western Research Laboratory, research Report 95-3, pp. 1-22.	
	5.	JUTTNER et al., "Lagrange Relaxation Based Method for the QoS Routing Problem, IEEE, Apr. 26, 2001, pp 259-268.	
	6.	SCHULZ U., Hierarchical Physical Design System, CompEuro '89, VLSI and Computer Peripherals. VSLI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks. Proceedings, 8-12 May 1989, pp. 5/20 - 5/24.	
	7.	TSENG H-P. et al., A Gridless Multilayer Router for Standard Cell Circuits Using CTM Cells, Oct. 1999, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 18, iss. 10, pp. 1462-1479.	

Examiner Date Signature Considered			
Signature Considered	Examiner	Date	
	Signature	Considered	

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